

FIG. 1 (PRIOR ART)

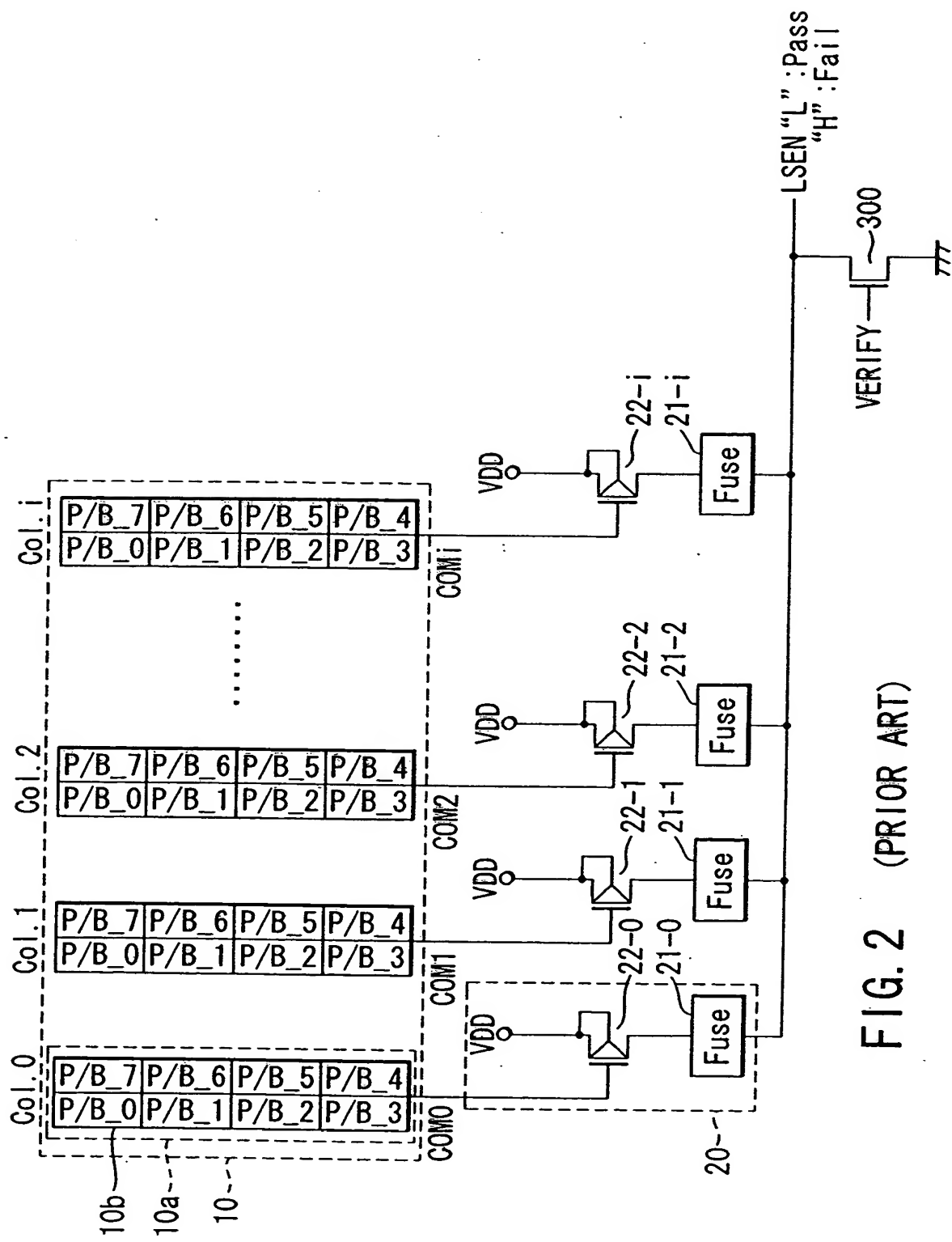
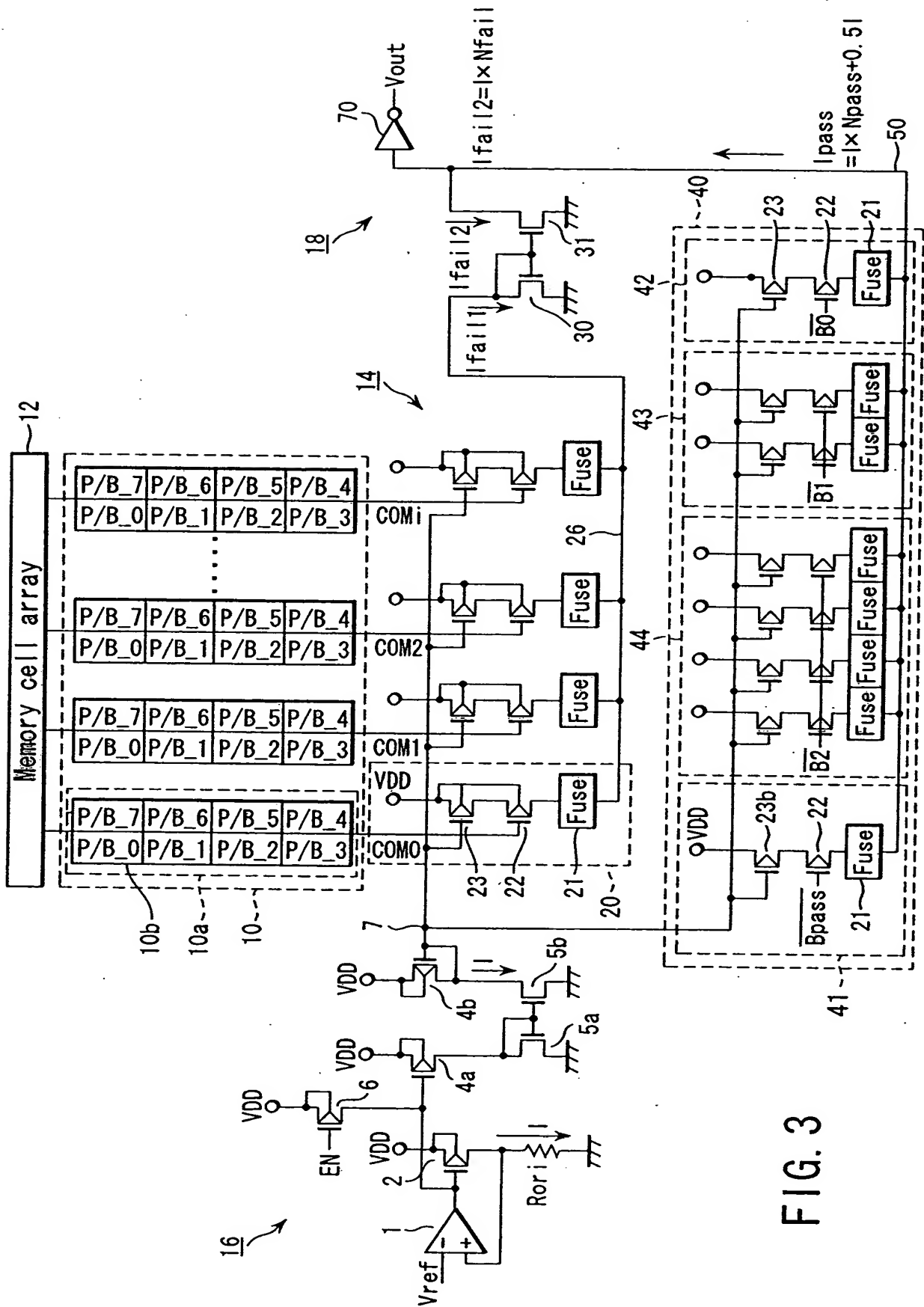


FIG. 2 (PRIOR ART)



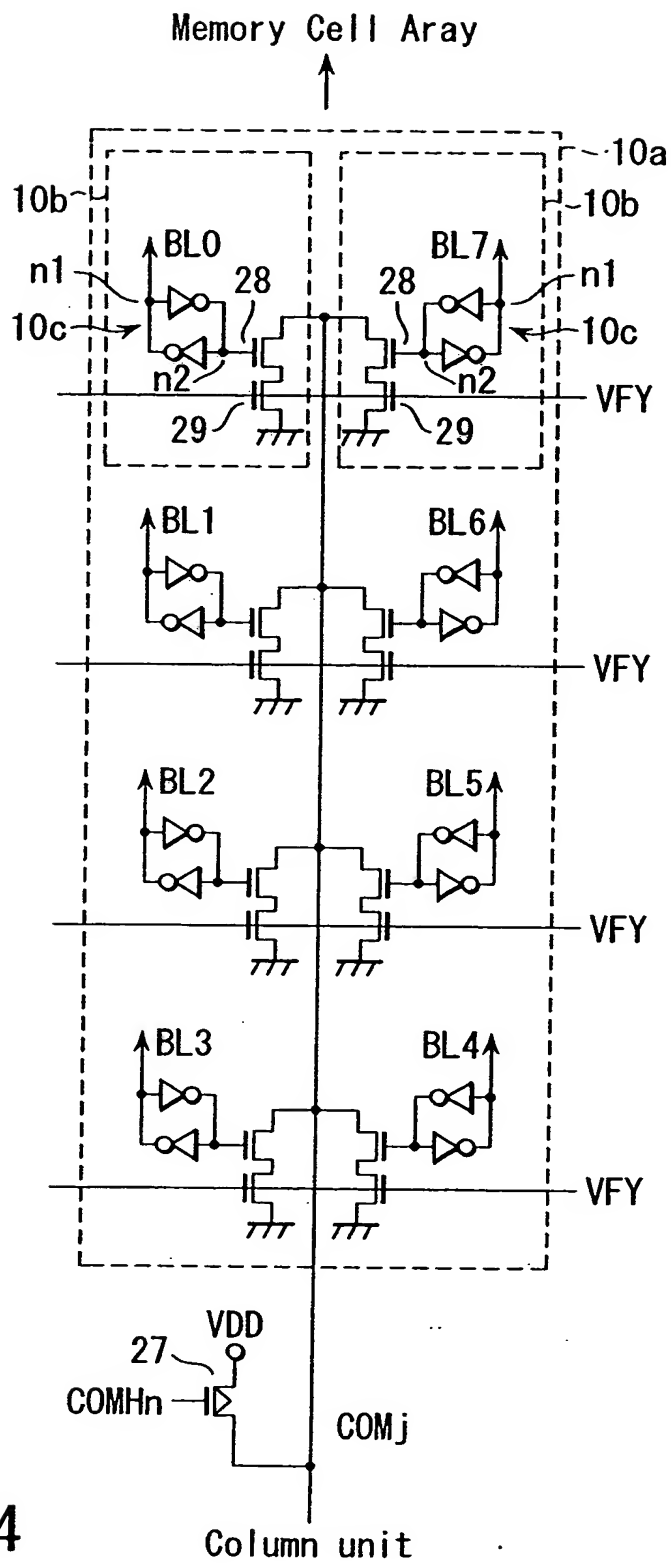


FIG. 4

FIG. 5

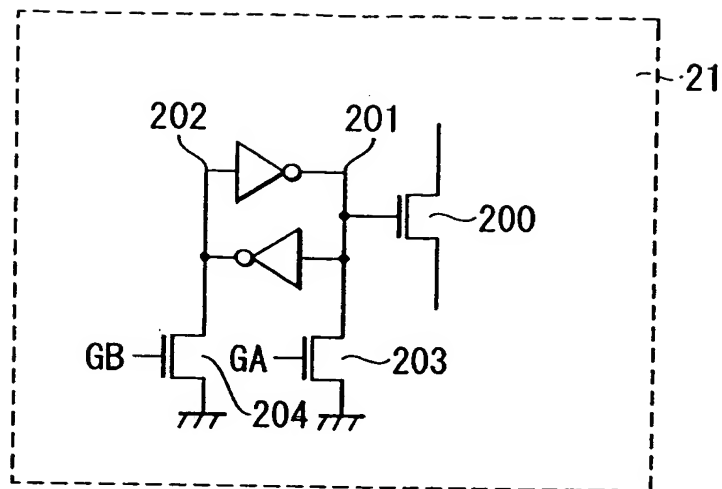
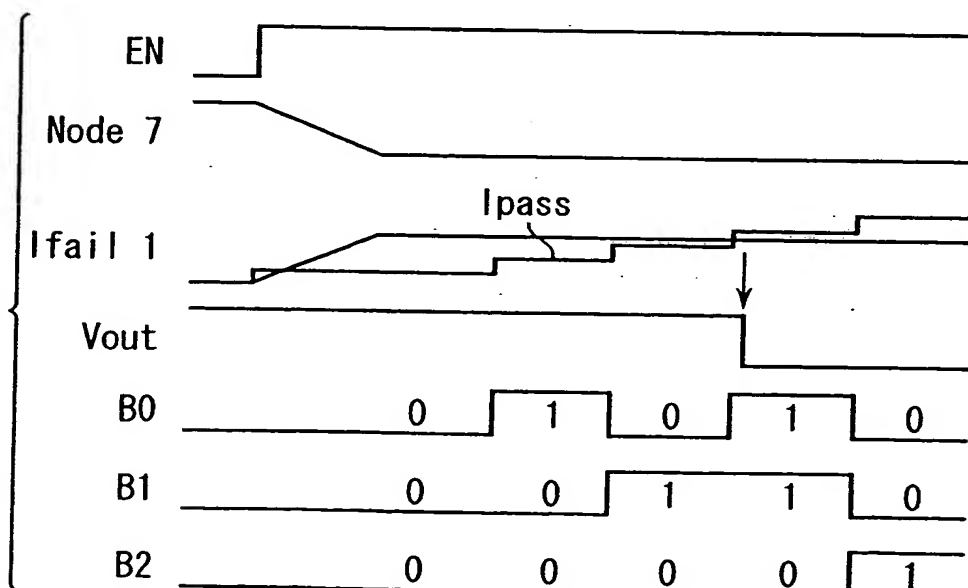
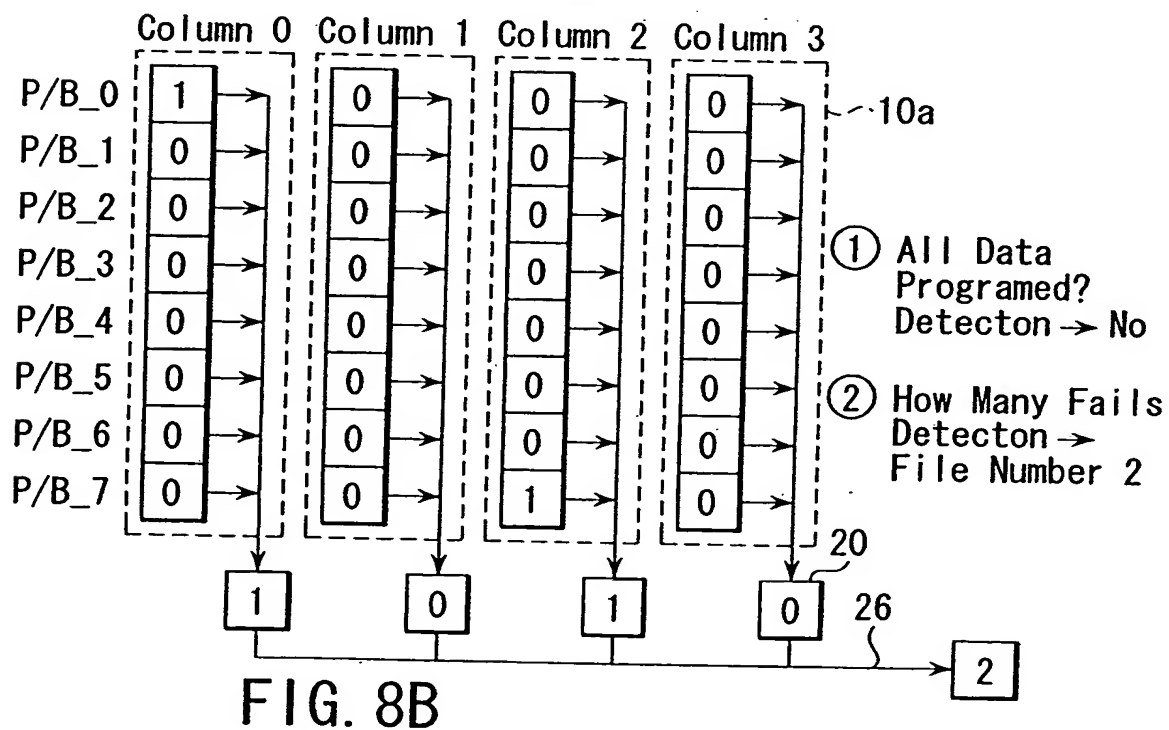
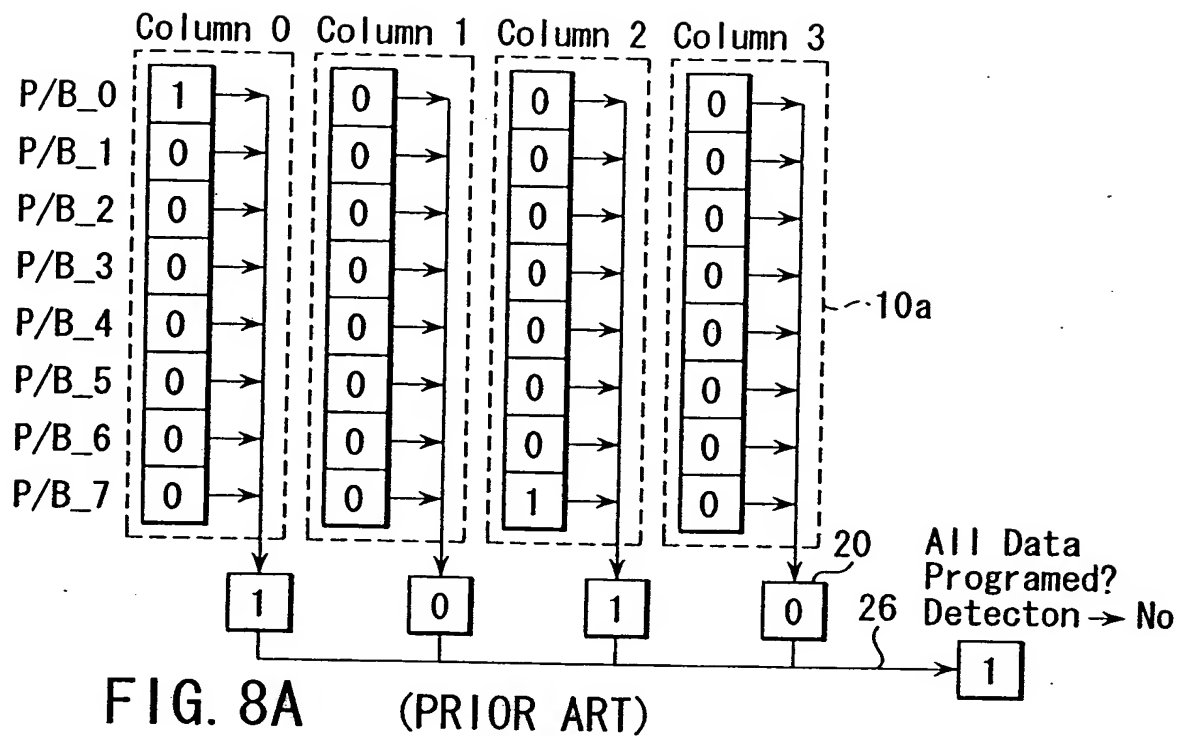


FIG. 6

(B2, B1, B0)	I_{pass}	Detection Number of Fails
(0, 0, 0)	0.5I	≥ 1
(0, 0, 1)	1.5I	≥ 2
(0, 1, 0)	2.5I	≥ 3
(0, 1, 1)	3.5I	≥ 4
(1, 0, 0)	4.5I	≥ 5
(1, 0, 1)	5.5I	≥ 6
(1, 1, 0)	6.5I	≥ 7
(1, 1, 1)	7.5I	≥ 8

FIG. 7





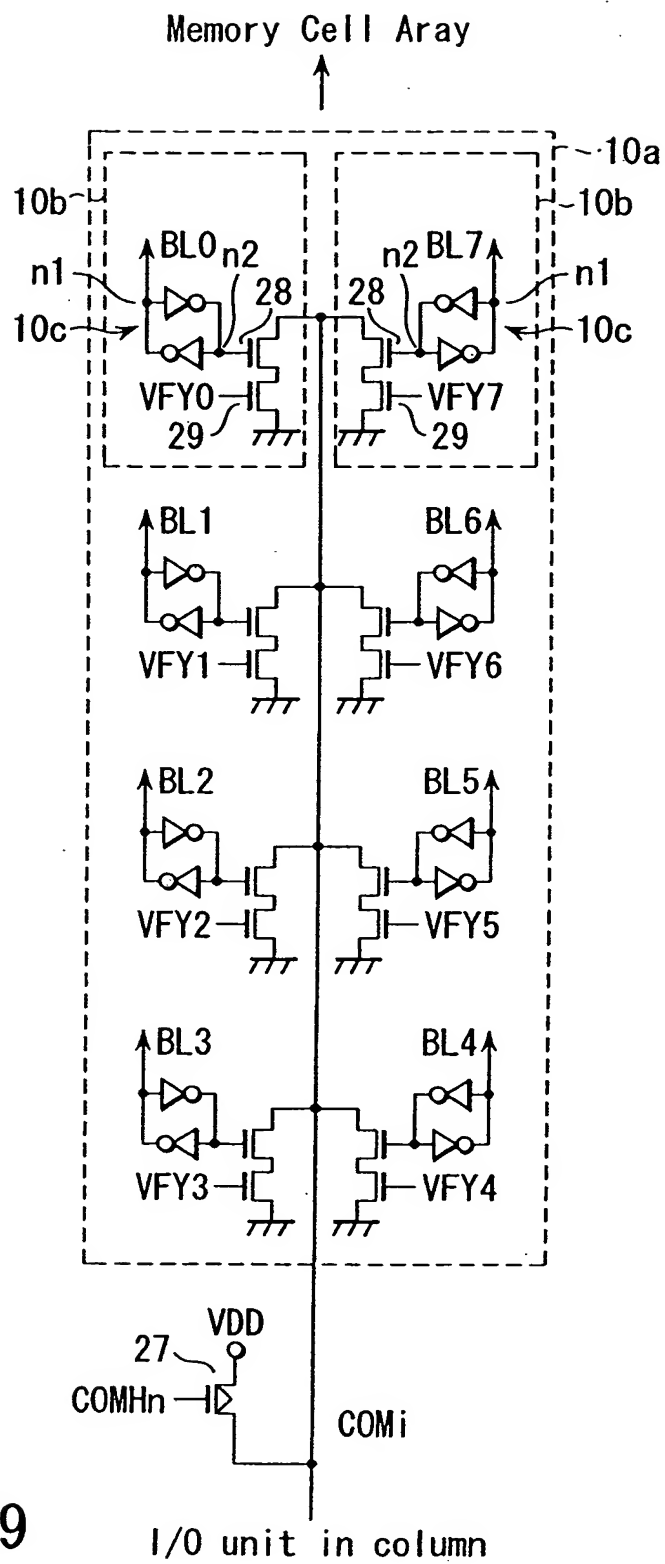
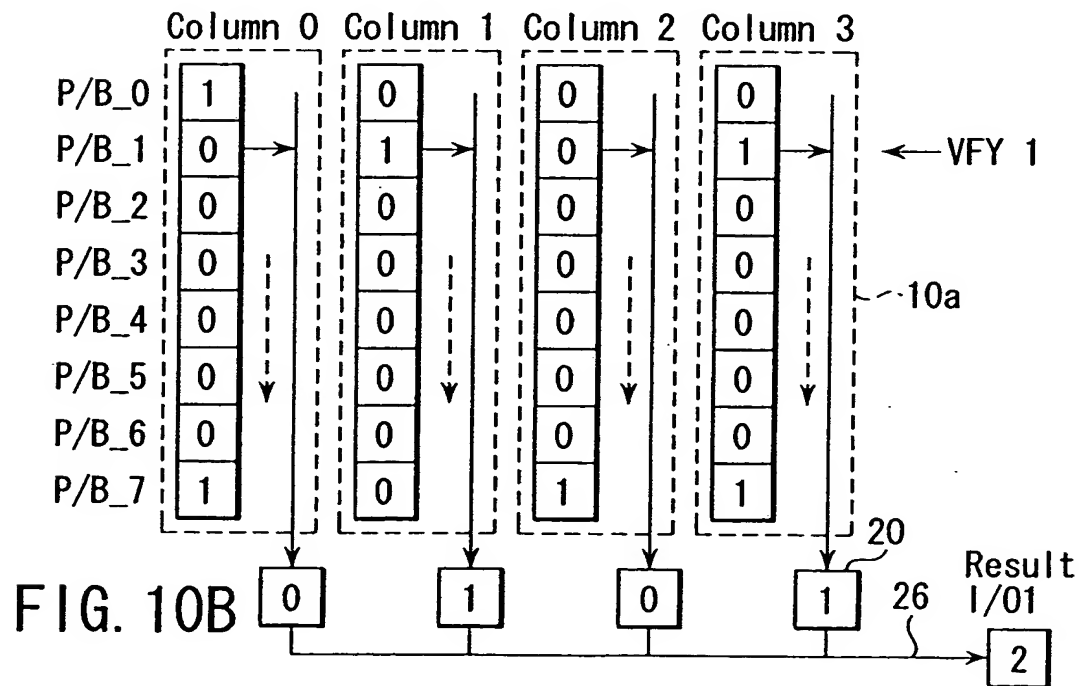
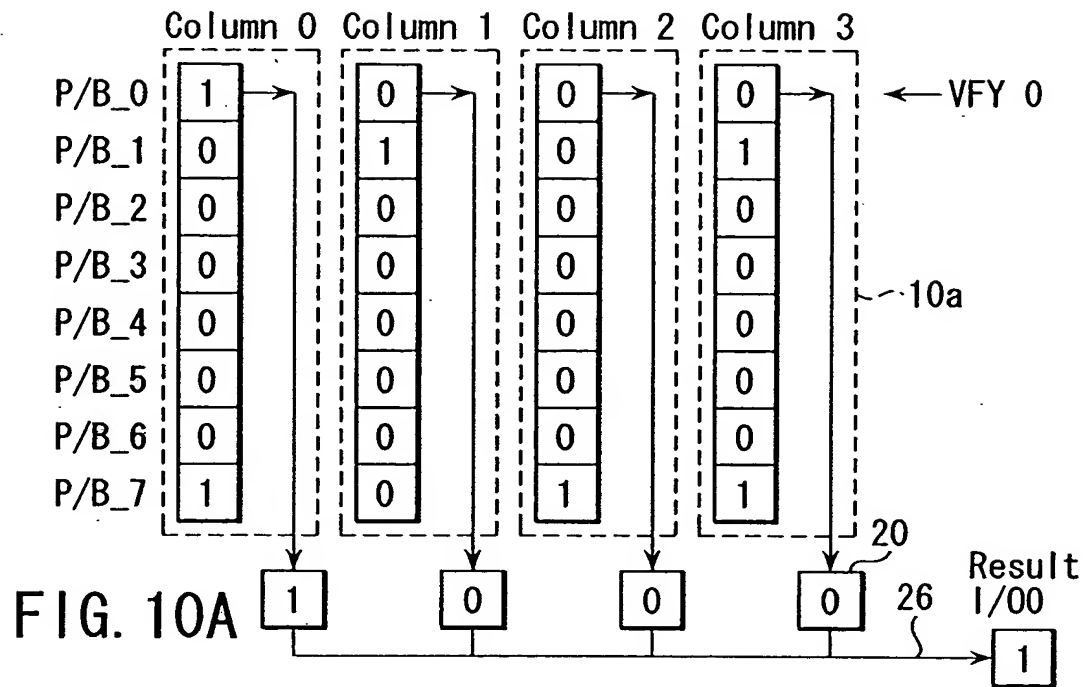


FIG. 9



I/00	1	}	Total Fail Bits:6
I/01	2		
I/02	0		
I/03	0		
I/04	0		
I/05	0		
I/06	0		
I/07	3		

FIG. 11

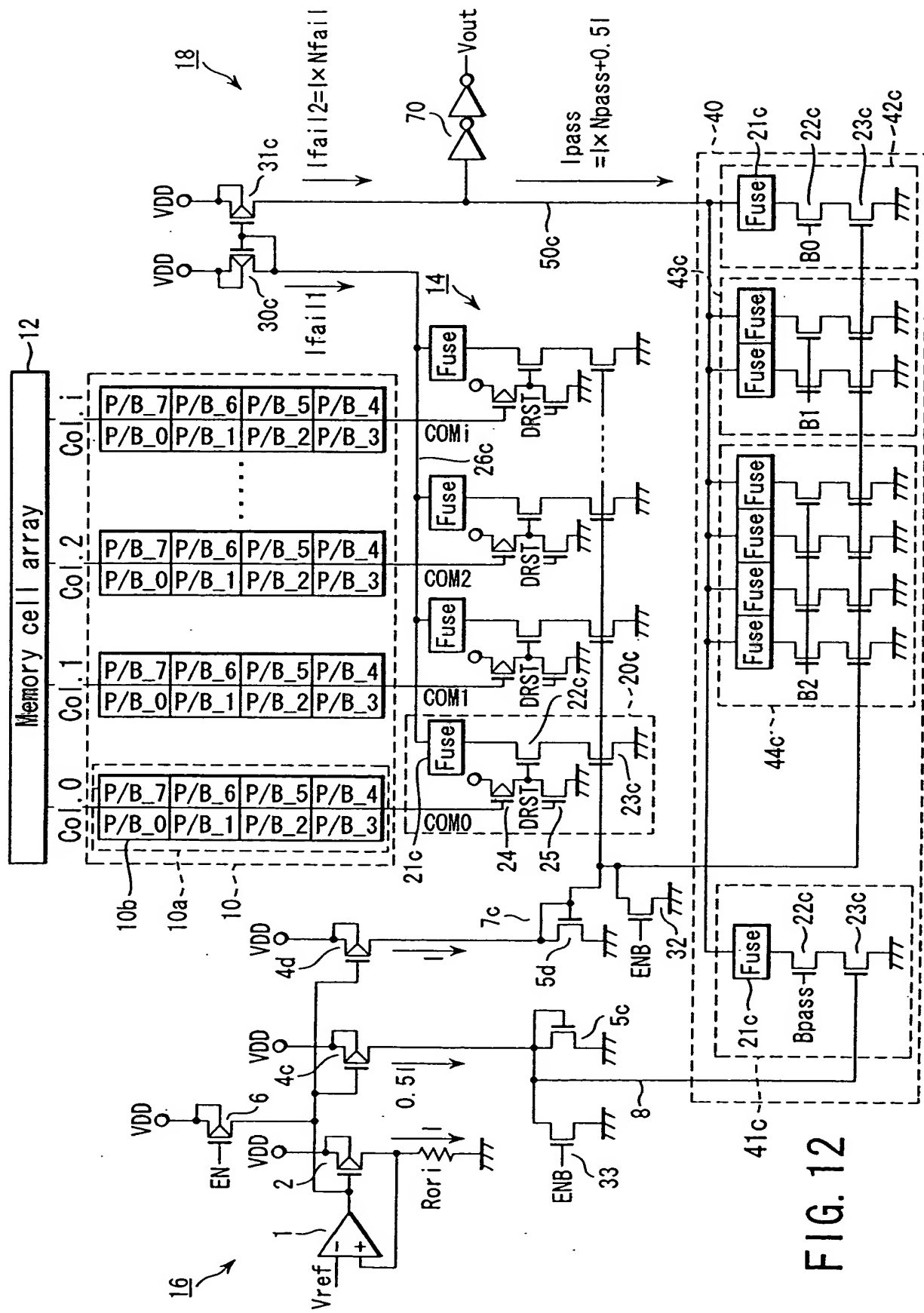


FIG. 12